



# Specification for Approval

		Date: 2024/1/1	_
	Custome	r:	
	BYTEK P/N:	BAM0806NF-900T0	01
	CUSTOMER P/N:		
	DESCRIPTION:		
	QUANTITY:	pcs	; -
REMA	RK:		
	Cu	stomer Approval Feedbac	k

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BYTEK P1

## **Multilayer Common Mode Choke Coils**

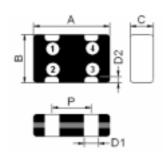
BAM0806NF-900T01

#### 1. Scope

This specification applies to Multilayer Common Mode Choke Coil, BAM Series Its Application is limited for the High speed differential transmission line like as followings.

USB, LVDS, MIPI, MDDI, MHL, HDMI, DVI.

#### 2. Dimensions





Chip Size							
Size	Α	В	С	Р	D1	D2	
0806	0.85±0.05	0.65±0.05	0.40 ±0.05	0.50±0.10	0.27±0.10	0.20+0.05/-0.1	

Units: mm

#### 3. Part Numbering



A: Series

B: Dimension A x B

C: Material Lead Free Code

D: Impedance Common Mode Impedance 900=90
E: Packaging T=Taping and Reel , B=Bulk(Bags)

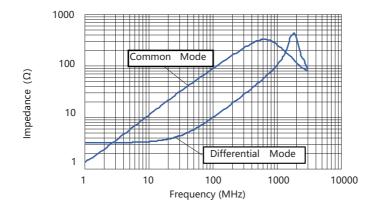
F: Rated Current 01=100mA

# Common-mode (Differential-mode) HP-4291A HP-4291A

#### 4. Specification

Part Nilmher	Common Mode Impedance ( )	Test Frequency (MHz)	Rated Voltage (Vdc) max.	Insulation Resistance (M ) min.	DC Resistance ( ) max.	Rated Current (mA) max.
BAM0806NF-900T01	90±20%	100	5	100	2.5	130

■ Impedance-Frequency Characteristics



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### 5. Reliability and Test Condition

Item	Performance		Test C	ondition	
Series No.		ВАМ	-		
Operating Temperature	(	-40~+85 Including self-generated heat)	-		
Transportation Storage Temperature		-40~+85	For long storage con Application Notice	nditions, please see the	
Impedance (Z)			Measuring equipment:42 Measuring jig: 16192A (		
Insulation Resistance	-Within the specified tolerar	00	Measuring points: 1 to 2 Measuring voltage: Rate		
DC Resistance	within the specified tolerar	ice	Measuring points: 1 to	2 or 3 to 4	
Rated Current	-				
	Per table 1.  Table 1			dered to test board and the under the conditions shown	
	Appearance	No remarkable Defect	Vibraiton frequency	10Hz to 55Hz	
Vibration	Commom	Within±20%	overall amplitude	1.5mm	
	Impedance change rate  Insulation	100mΩ min	1 cycle Time	1min.(10 55 10Hz)  X 2 hours  Y each	
Solderability	More than 75% of terminal	electrode shall be covered with fresh solder.	Test sample shall be immersed into molten sold under the conditions shown in Table 3 aft immersed into flux.  After this, test samples shall be taken out ar visually checked.  The speed for immersion and taking out shall be 2 mm/s.  Table 3  Solder temperature 245 ±3  Immersion time 4s±1s  Test sample shall be immersed into molten sold after immersed into flux and preheated under the conditions shown in Table 4.		
Resistance to Soldering Heat	Per table 1.		measured after kept at r hours.(Note 1)	s shall be taken out and coom temperature for 2 to 3 on and taking out shall be 150 ,3min. 260 ±5	
Thermal Shock	Per table 1.		Steps 1 to 4 shown in T repeated 5 times.  After the test, keep the	Table 5 as one cycle shall be at a normal all humidity for 2 to 2 hours be conducted.(Note 1)  Tre( ) Duration (min)  3	
Resistance to Humidity	Per table 1.		temperature of 40 ±2 90% to 95% for 500+24/- After the test, keep the	e test sample at a normal al humidity for 2 to 3 hours,	
High Temperature Load Life Test	Per table 1.		temperature of 85 ±2 supplying 1 to 2 and After the test, keep the	in a thermostatic oven with for 500+24/-0 hours while 3 - 4 with rated current. test sample at a normal al humidity for 2 to 3 hours,	

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Item	Performance	Test Condition
High Temperature Life Test	Per table 1.	Test board shall be kept in an atmosphere with temperature of 85 ±2 for 500+24/-0 hours. After the test, keep the test sample at a normal temperature with a normal humidity for 2 to 3 hours, then measurement shall be conducted.(Note 1)
	Appearance: No mechanical damage.  Board R-230 Warp  Warp  (Unit:mm)	Warp : 2mm(1210),1mm(0806) Testing board : Class epoxy-resin substrate Thickness : 0.8mm

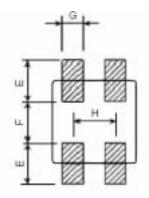
(Note 1) If question is found in the result of measurement, another measurement shall be conducted after test samples shall be kept for 48+/-2 hours.

#### 6. Soldering and Mounting

#### 6-1. Recommended PC Board Pattern

Chip Size								terns Fo	
Туре	Α	В	С	D1	D2	E	F	G	Н
0806	0.85±0.05	0.65±0.05	0.40 ±0.05	0.27±0.10	0.20+0.05/-0.1	0.25~0.35	0.25~0.35	0.25~0.35	0.5
1210	1.25±0.15	1.0±0.15	0.55 ±0.10	0.30±0.10	0.25+0.15/-0.1	0.45~0.55	0.7~0.8	0.25~0.35	0.55

Units: mm



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

Products shall be positioned in the sideway direction against the mechanical stress to prevent failure.

#### 6-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

Note.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

#### 6-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1.

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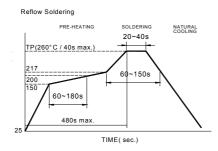
#### 6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

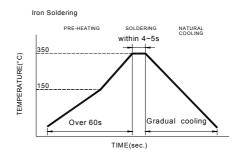
Preheat circuit and products to 150 350 tip temperature (max)

Never contact the ceramic with the iron tip 1.0mm tip diameter (max)

Use a 20 watt soldering iron with tip diameter of 1.0mm Limit soldering time to 4~5sec.



Reflow times: 3 times max



Iron Soldering times : 1 times max Fig.2

#### 6-2.3 Solder Volume:

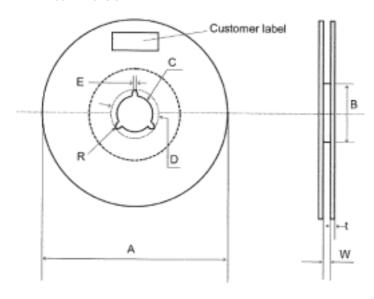
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



#### 7. Packaging Information

#### 7-1. Reel Dimension

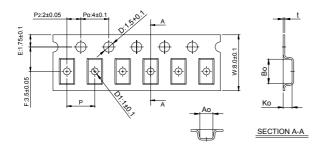


Code	Α	В	С	D	E	w	t	R
Dimension	178±2.0	50 min	13±0.2	21±0.8	2.0±0.5	10±1.5	2.5 max	1.0

Units: mm

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#### 7-2. Tape Dimension (paper)



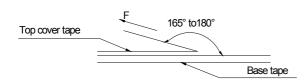
ĺ	Series	Во	Ao	Ko	Р	t
	0806	0.95±0.05	0.75±0.05	0.55±0.05	4.0±0.10	0.3 max
	1210	1.40±0.05	1.15±0.05	0.65±0.05	4.0±0.10	0.3 max

Units: mm

#### 7-3. Packaging Quantity

Chip size	0806	1210	
Chip /Reel	10000	4000	
Inner box	50000	20000	
Middle box	250000	100000	
Carton	500000	200000	

#### 7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed
( )	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

#### **Application Notice**

#### Storage Conditions

To maintain the solder ability of terminal electrodes:

- 1. BYTEK products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40 and 60% RH.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.

#### Transportation

- 1.Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.